

Amendments to the Specification:

On page 1, please amend paragraph [0001] as follows:

[0001] The present application is related to U.S. Patent Application Serial No. 10/689,256 ~~[[]]~~ entitled "Method for Finding Global Extrema of a Set of Bytes Distributed Across an Array of Parallel Processing Elements" filed ~~[[]]~~ ~~(DB001077-000, Micron no. 03-0053)~~ 20 October 2003, and U.S. Patent Application Serial No. 10/689,449 ~~[[]]~~ entitled "Method for Finding Global Extrema of a Set of Shorts Distributed Across an Array of Parallel Processing Elements" filed ~~[[]]~~ ~~(DB001078-000, Micron no. 03-0054)~~ 20 October 2003.

On page 8, please amend paragraph [0042] as follows:

[0042] The reader desiring more information about the hardware shown in FIGs. 1 - 3 is directed to UK Patent application 0221563.0 ~~(serial no. not yet assigned)~~ entitled "Control of Processing Elements in Parallel Processors" filed 17 September 2002, ~~(Micron no. 02-1604)~~ which is hereby incorporated by reference. Details about the PEs may also be found in UK Patent Application No. 021562.2 entitled "Host Memory Interface for a Parallel Processor" filed 17 September 2002, ~~(Micron no. 02-0703)~~ which is hereby incorporated by reference.